

APPROVED	O.G. FIG.	
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Title: Apparatus and Methods of Semiconductor  
 Wafer Processing Equipment  
 Inventor: Soucy et al.  
 Serial No. 09/897,820  
 Atty Docket No. APE-001  
 Atty: John V. Forcier

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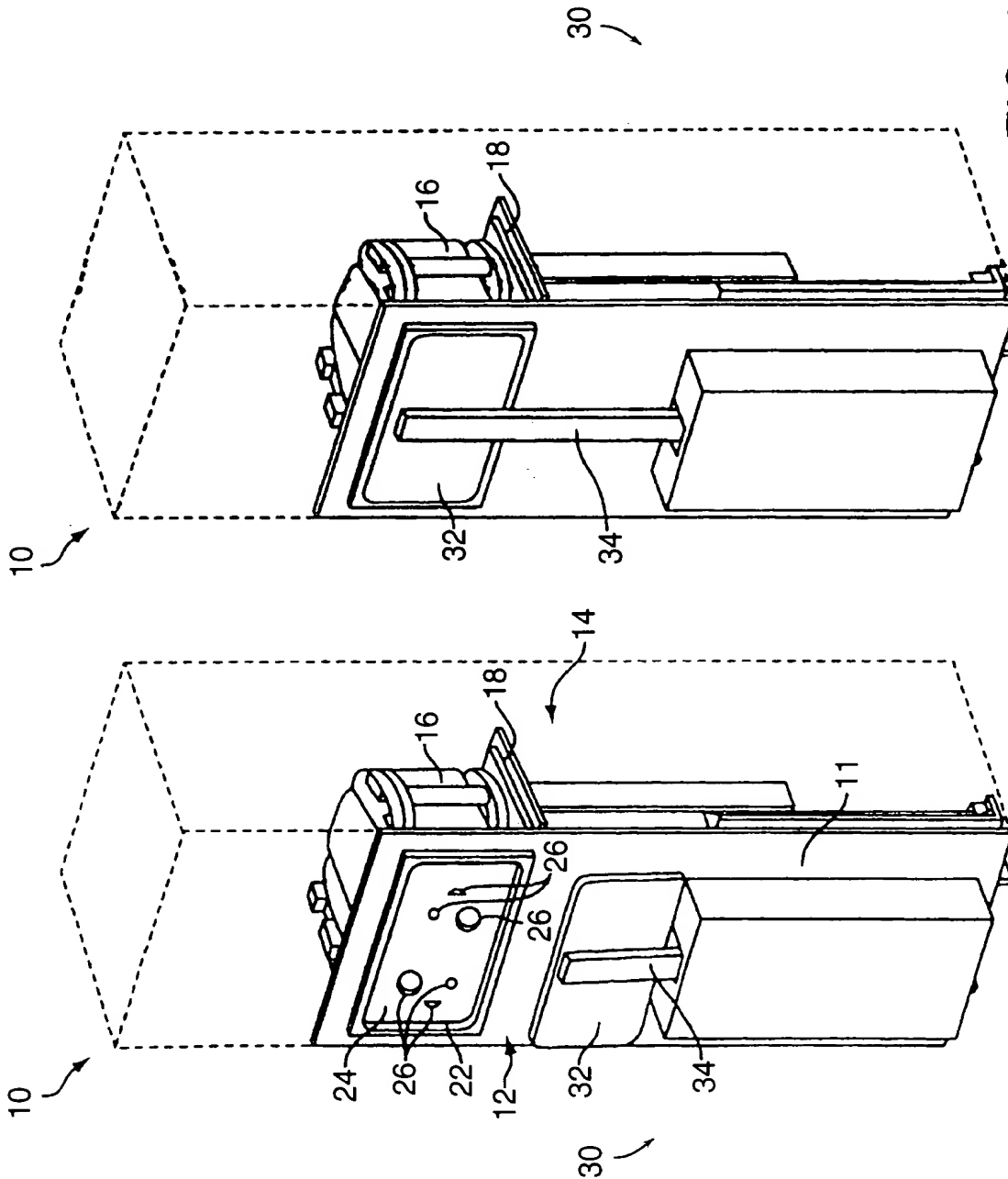


FIG. 1B  
 PRIOR ART

FIG. 1A  
 PRIOR ART

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FIG. 1D  
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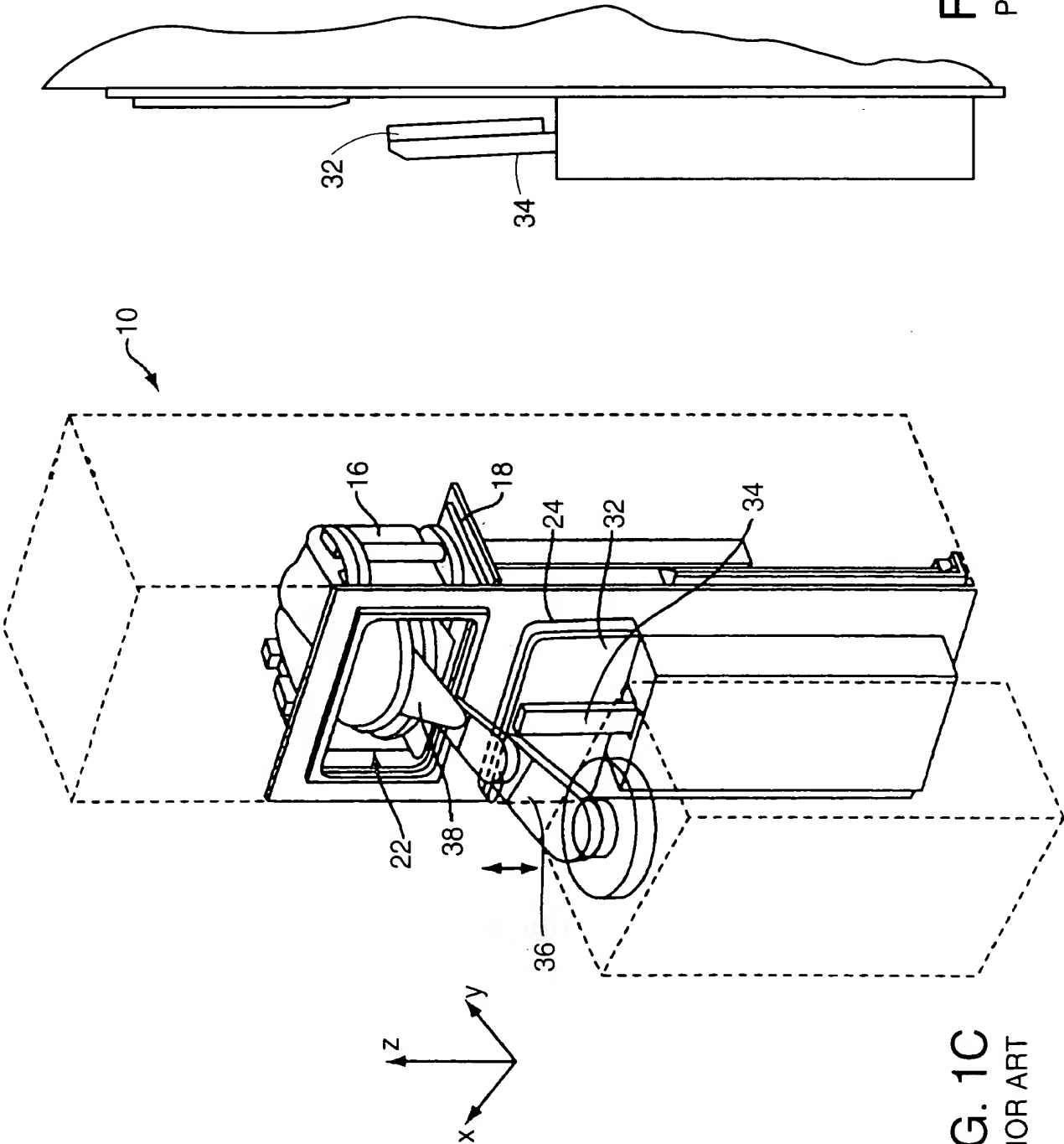


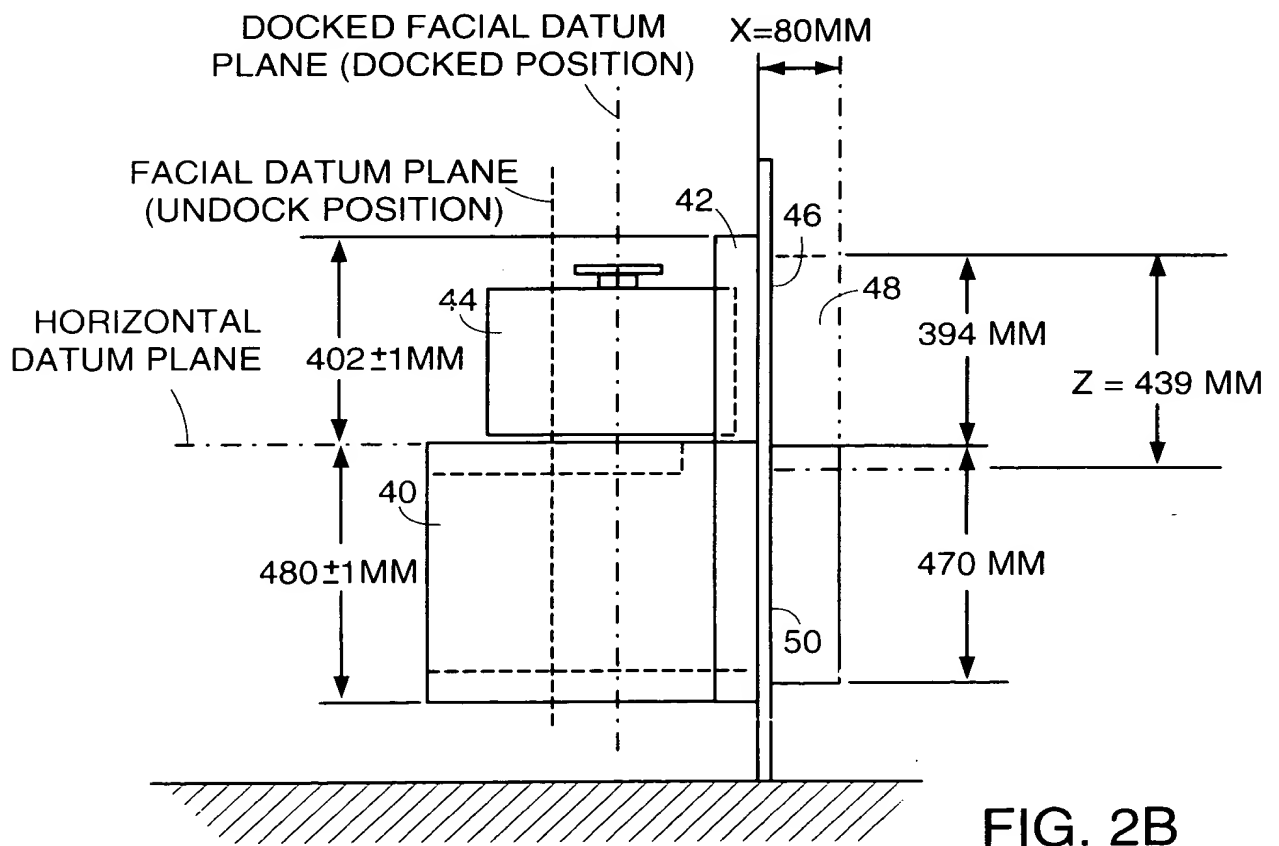
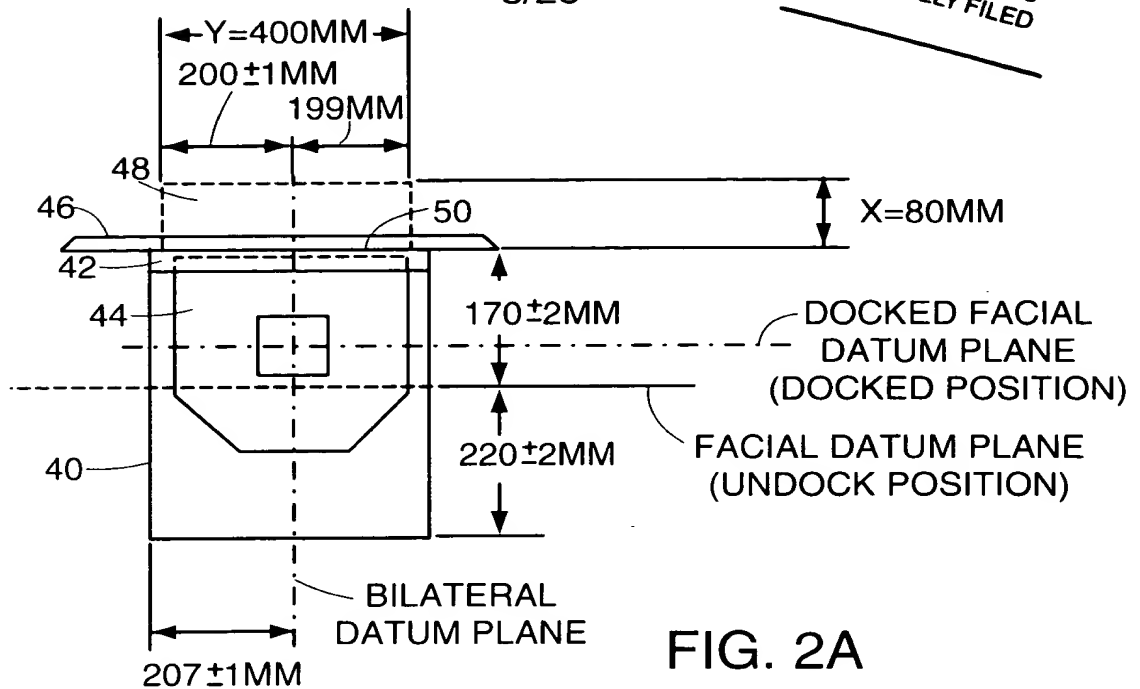
FIG. 1C  
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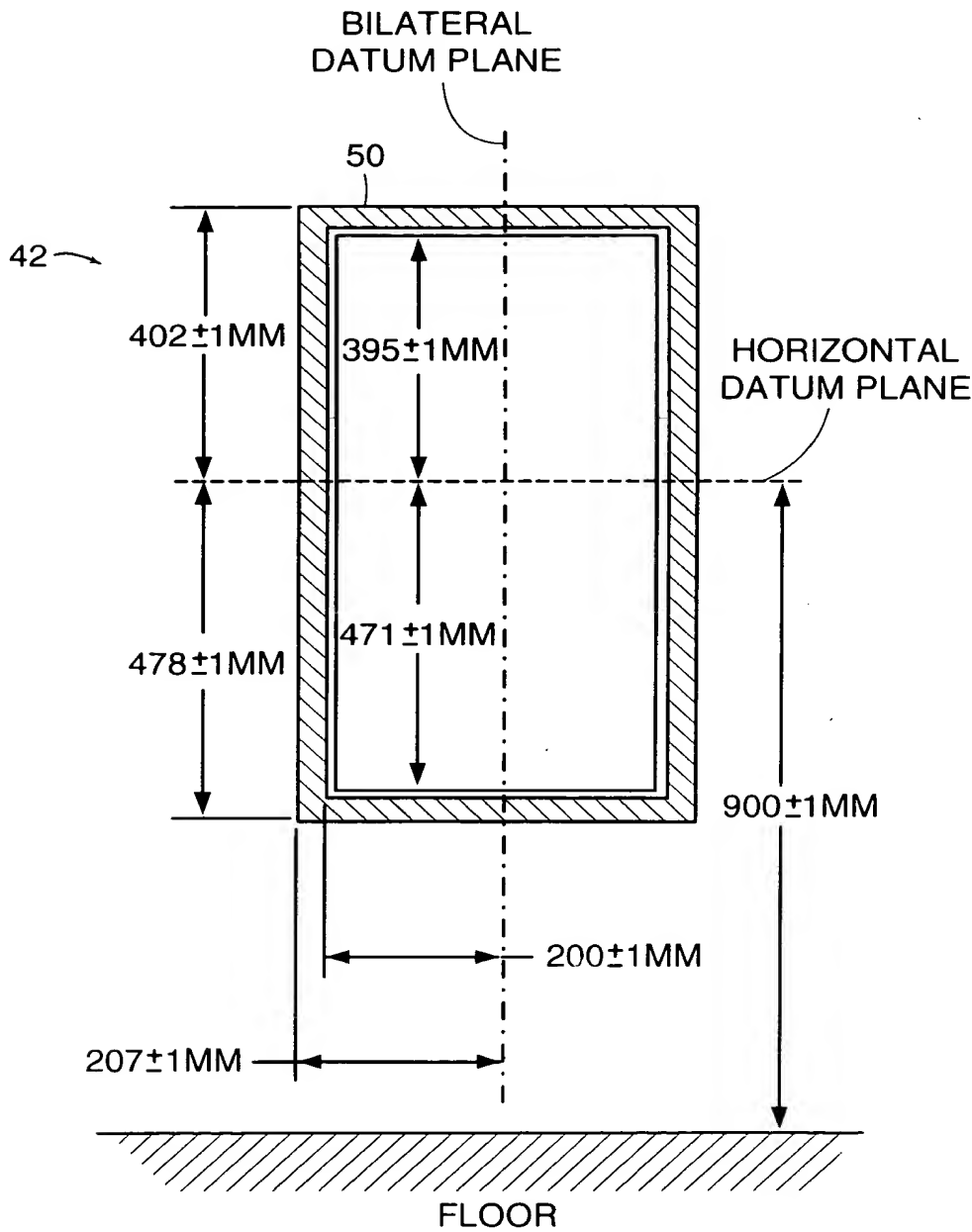


FIG. 2C



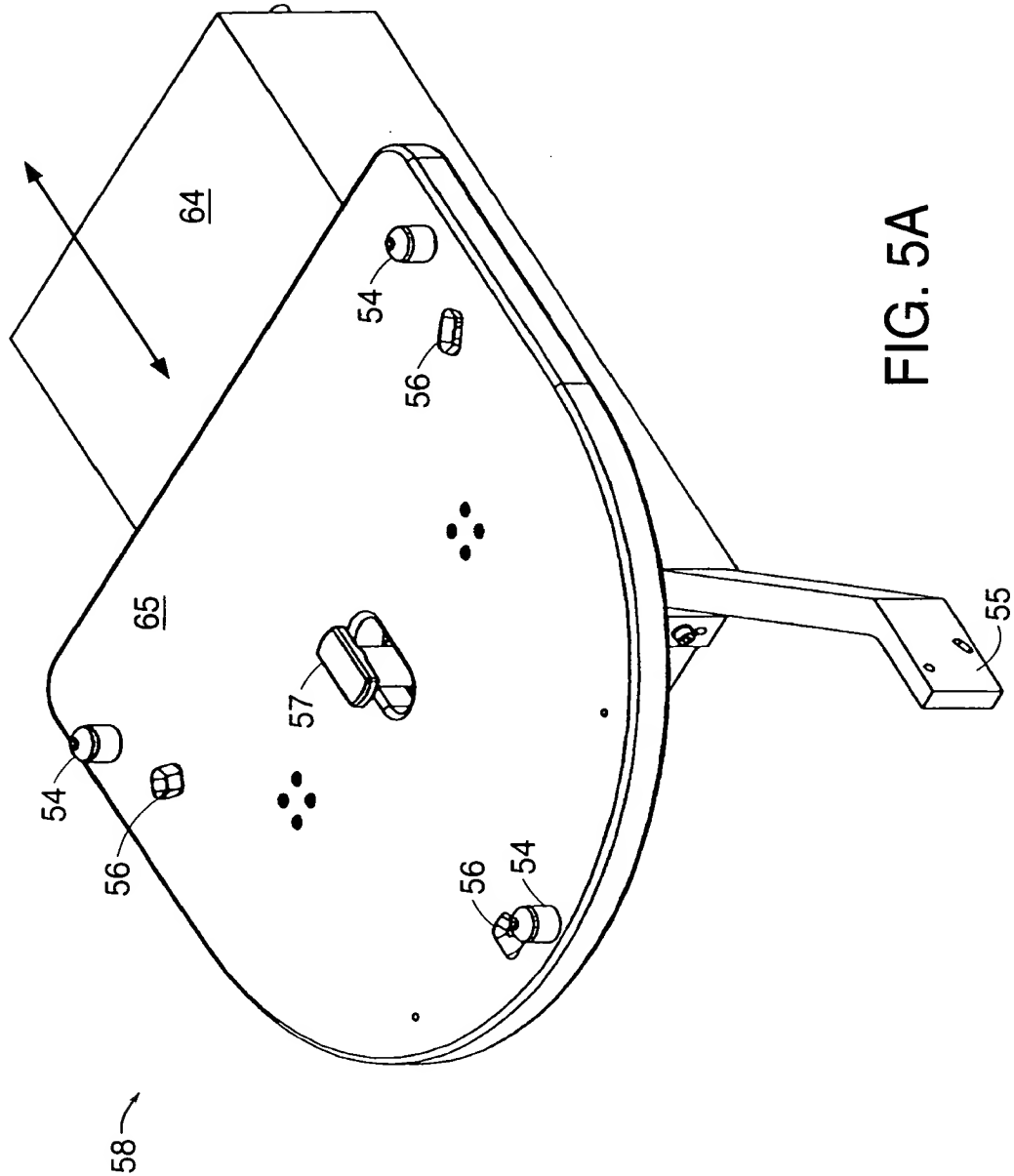


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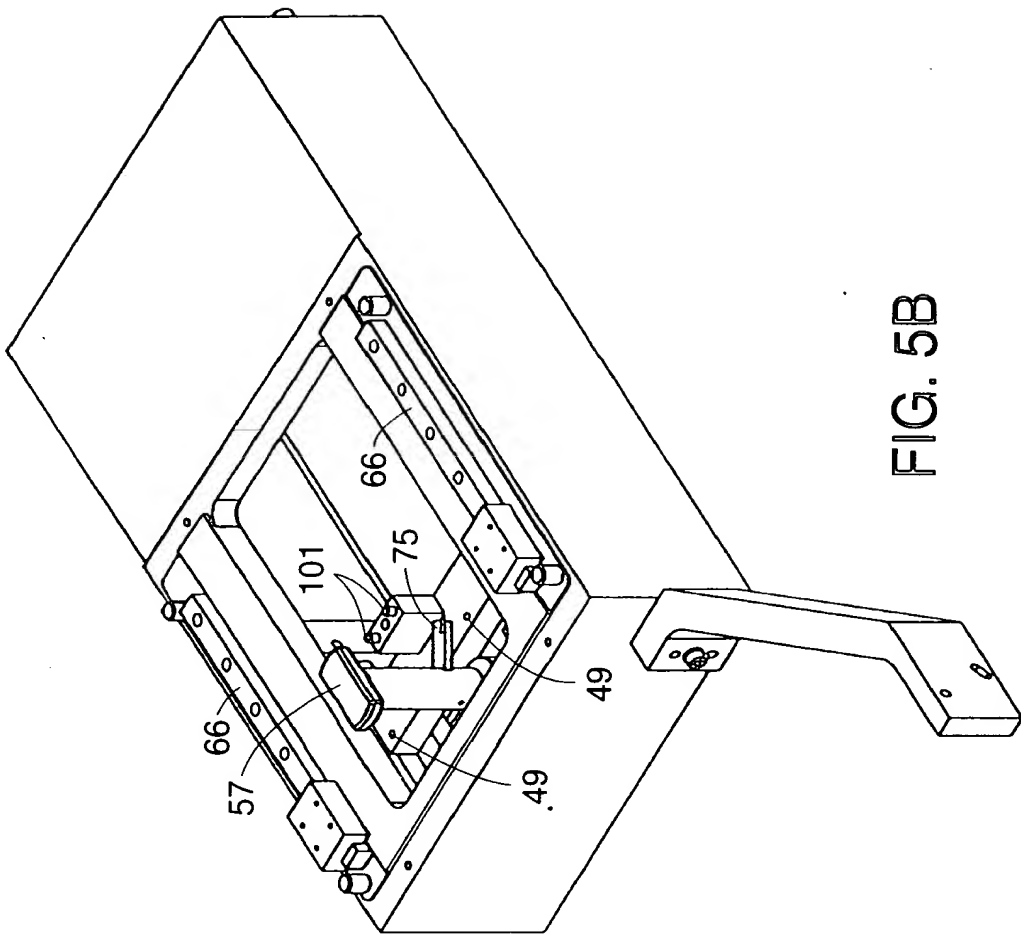


FIG. 5B



APPROVED	O.G. FIG.	
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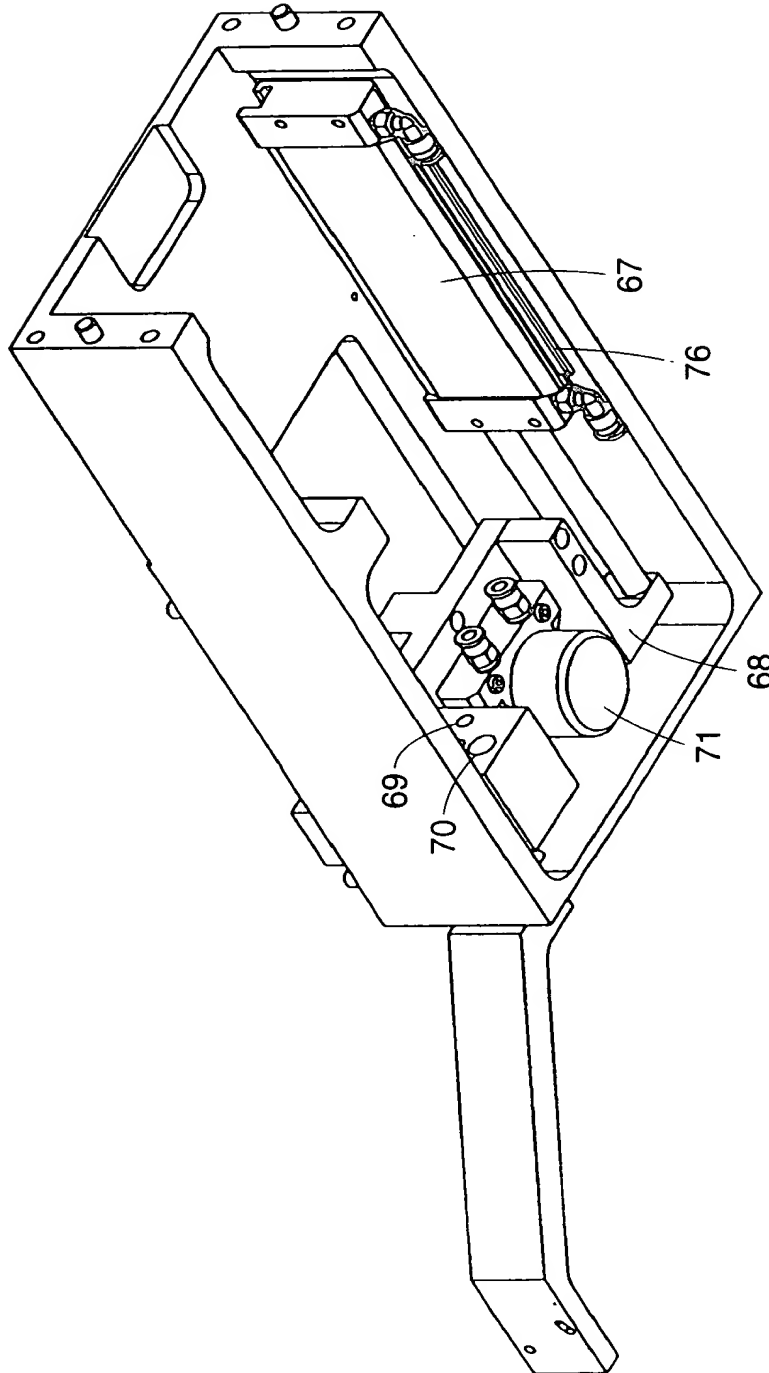


FIG. 5C

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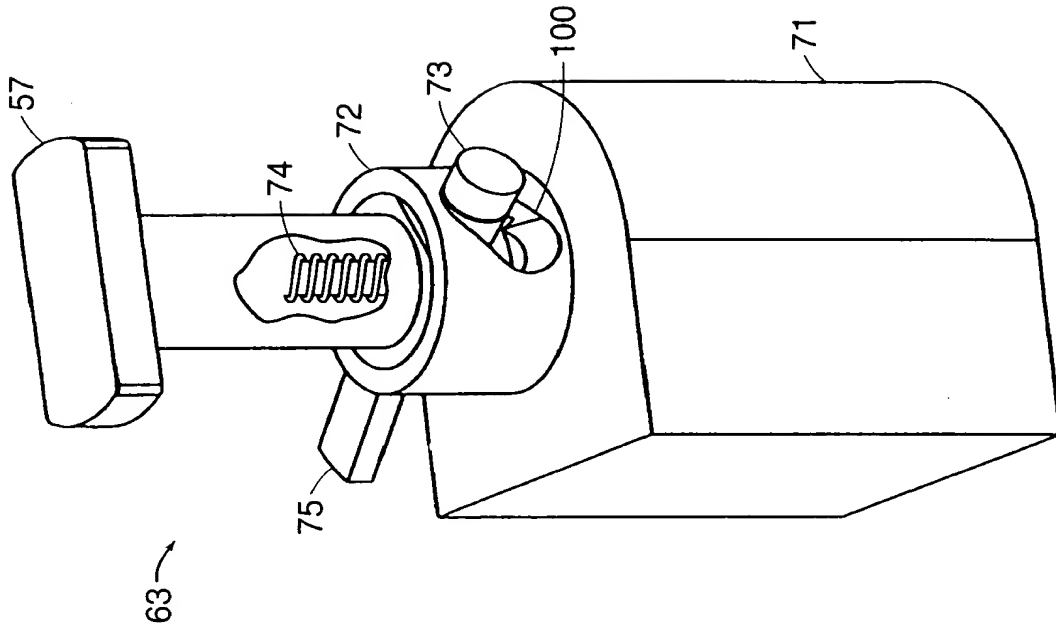


FIG. 5D

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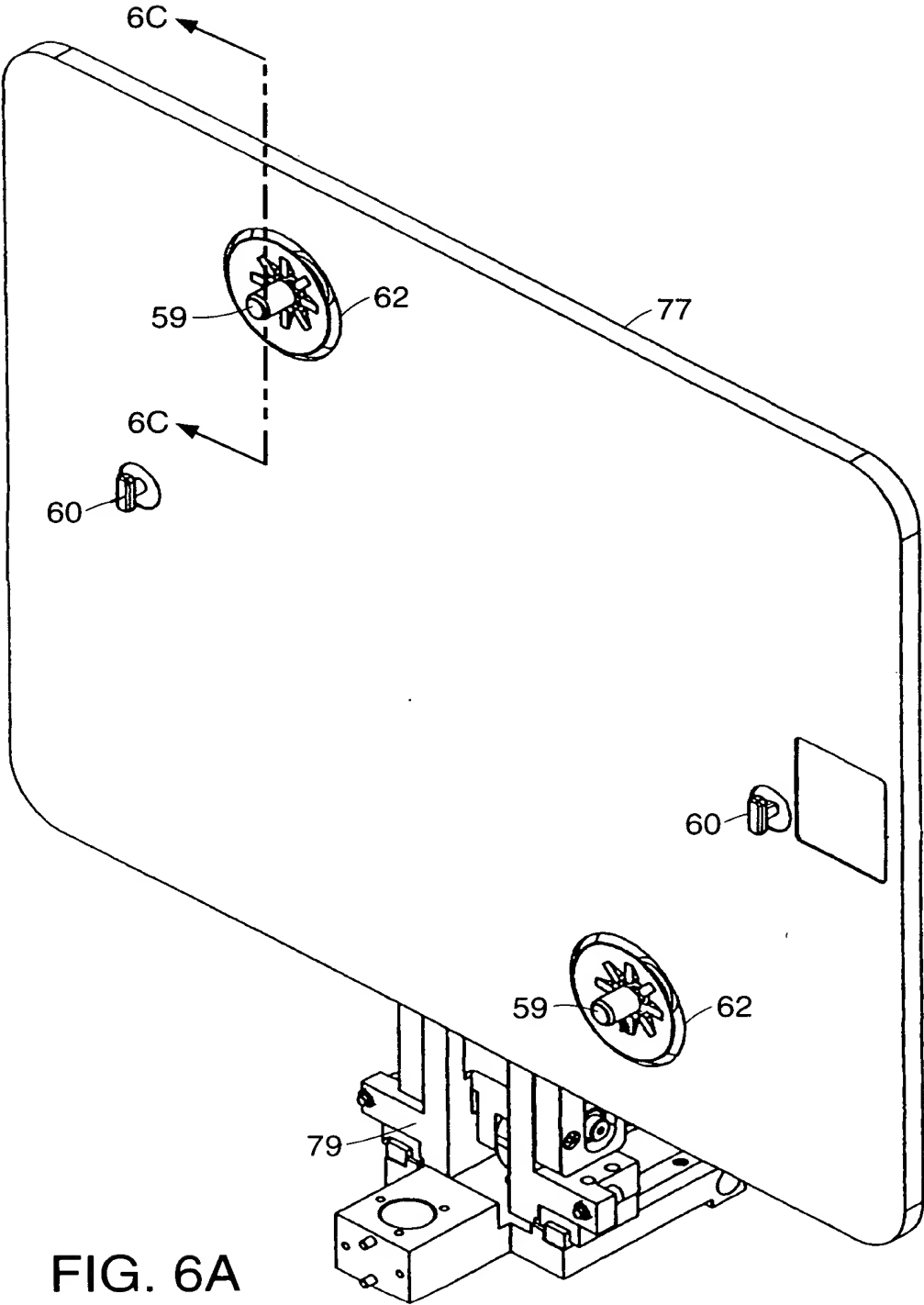


FIG. 6A

APPROVED	O.G. FIG.	
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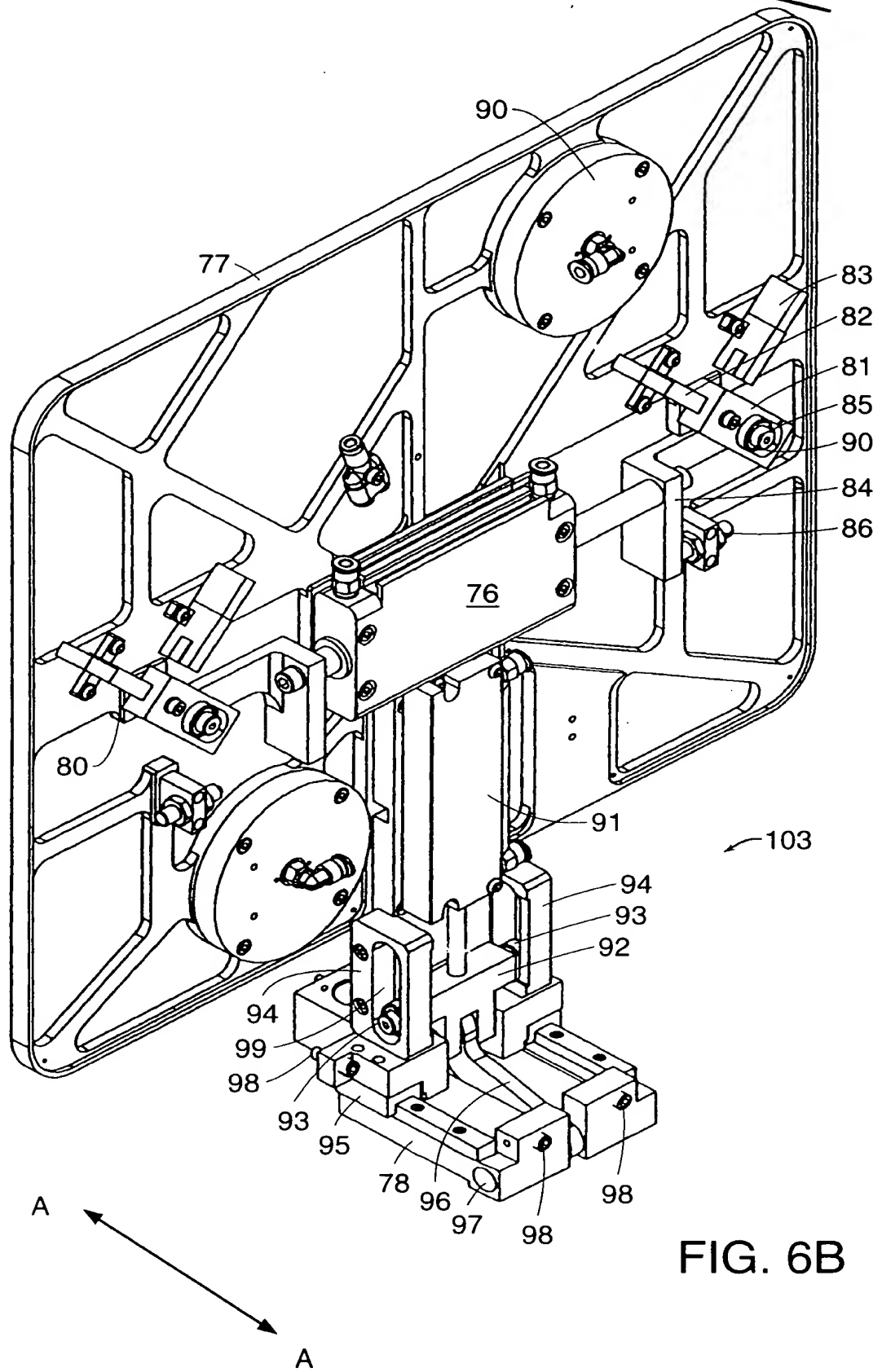


FIG. 6B

APPROVED	O.G. FIG.	
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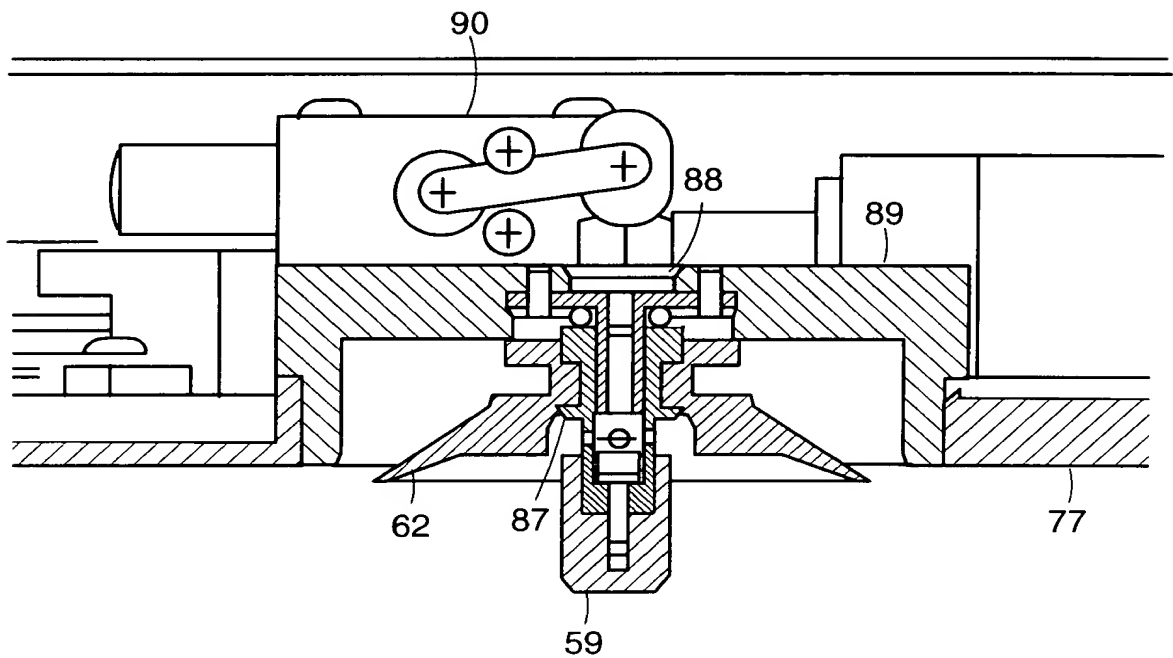


FIG. 6C

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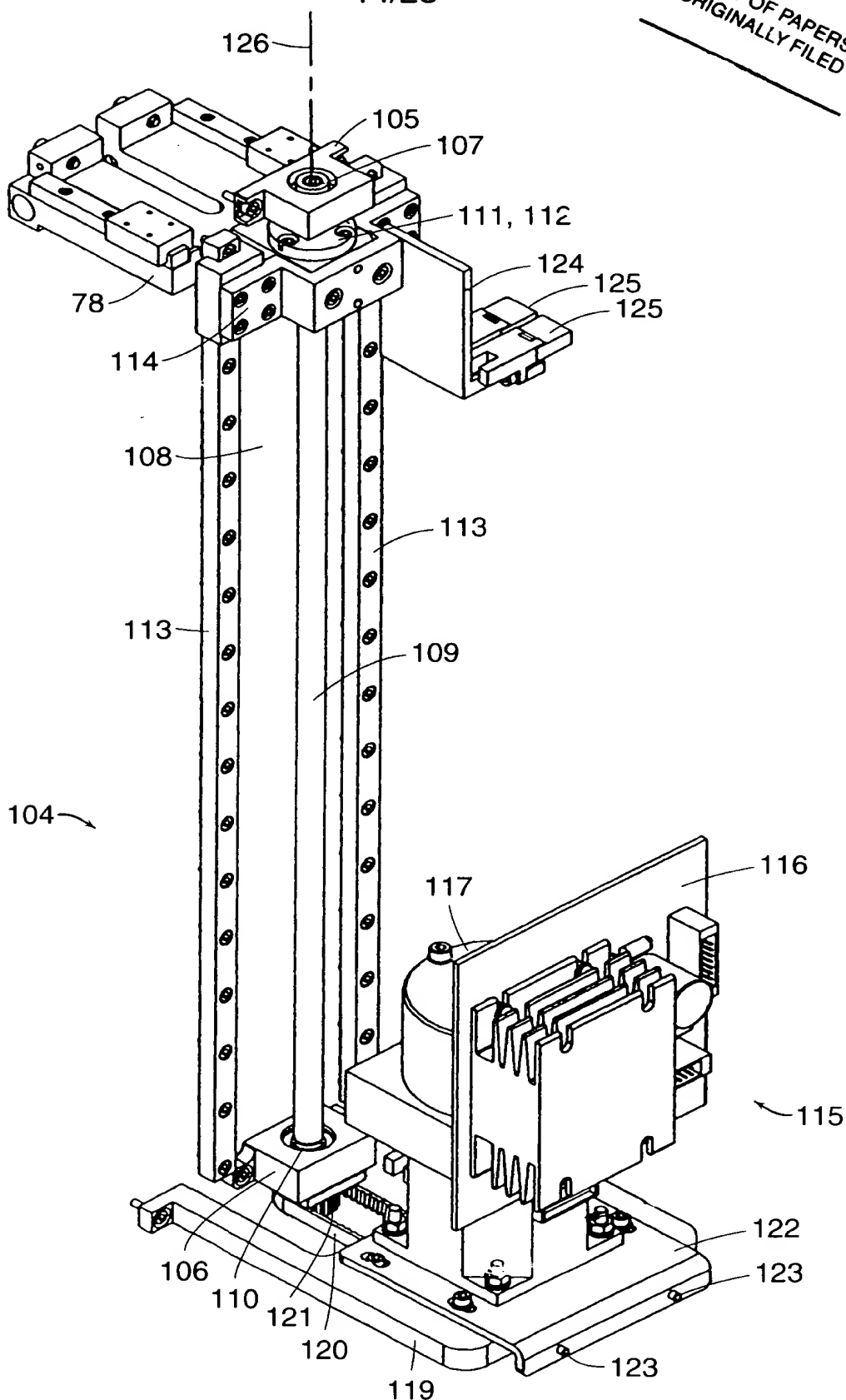


FIG. 7

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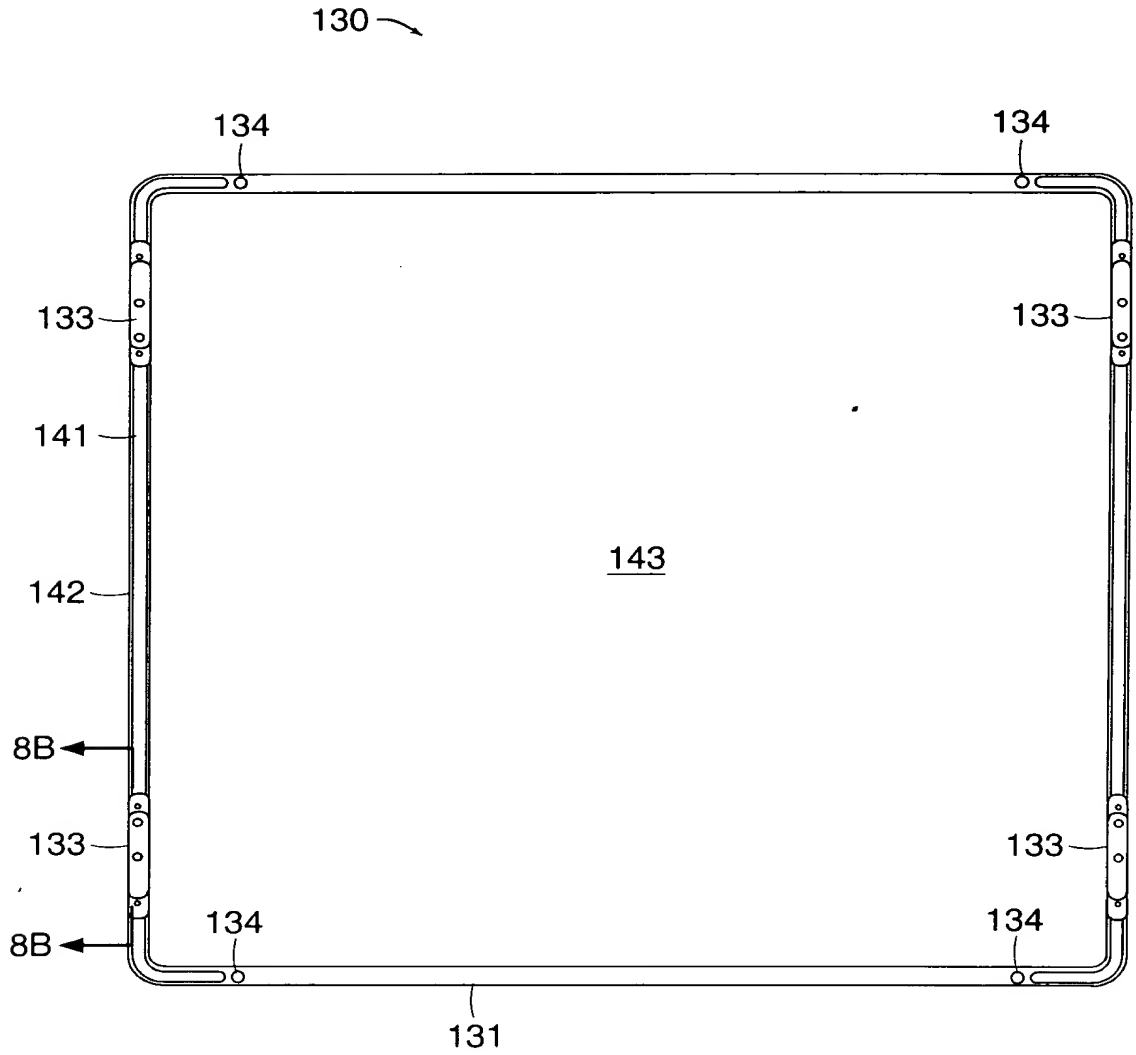


FIG. 8A

APPROVED	O.G. FIG.	
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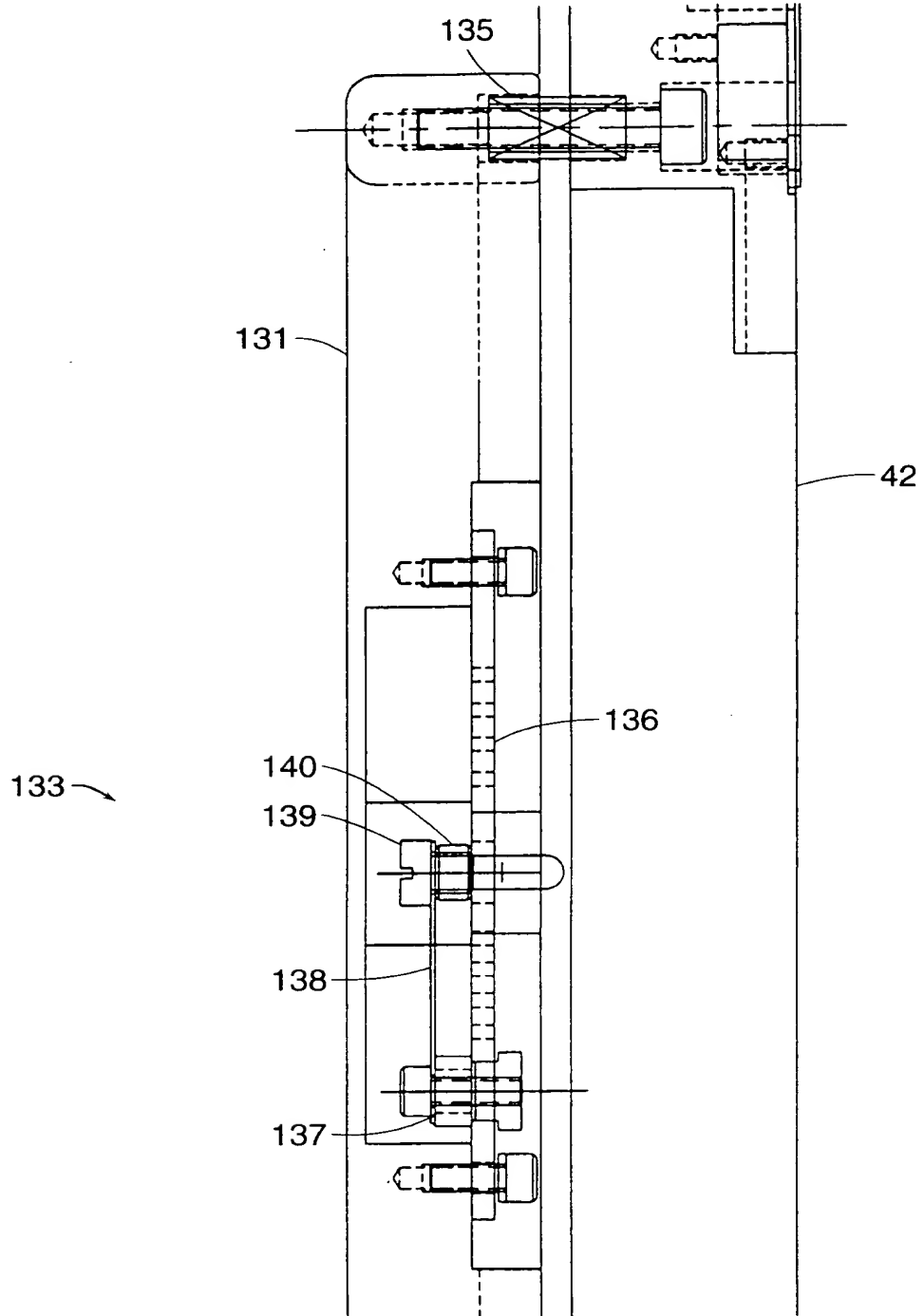


FIG. 8B



APPROVED	O.G. FIG.	
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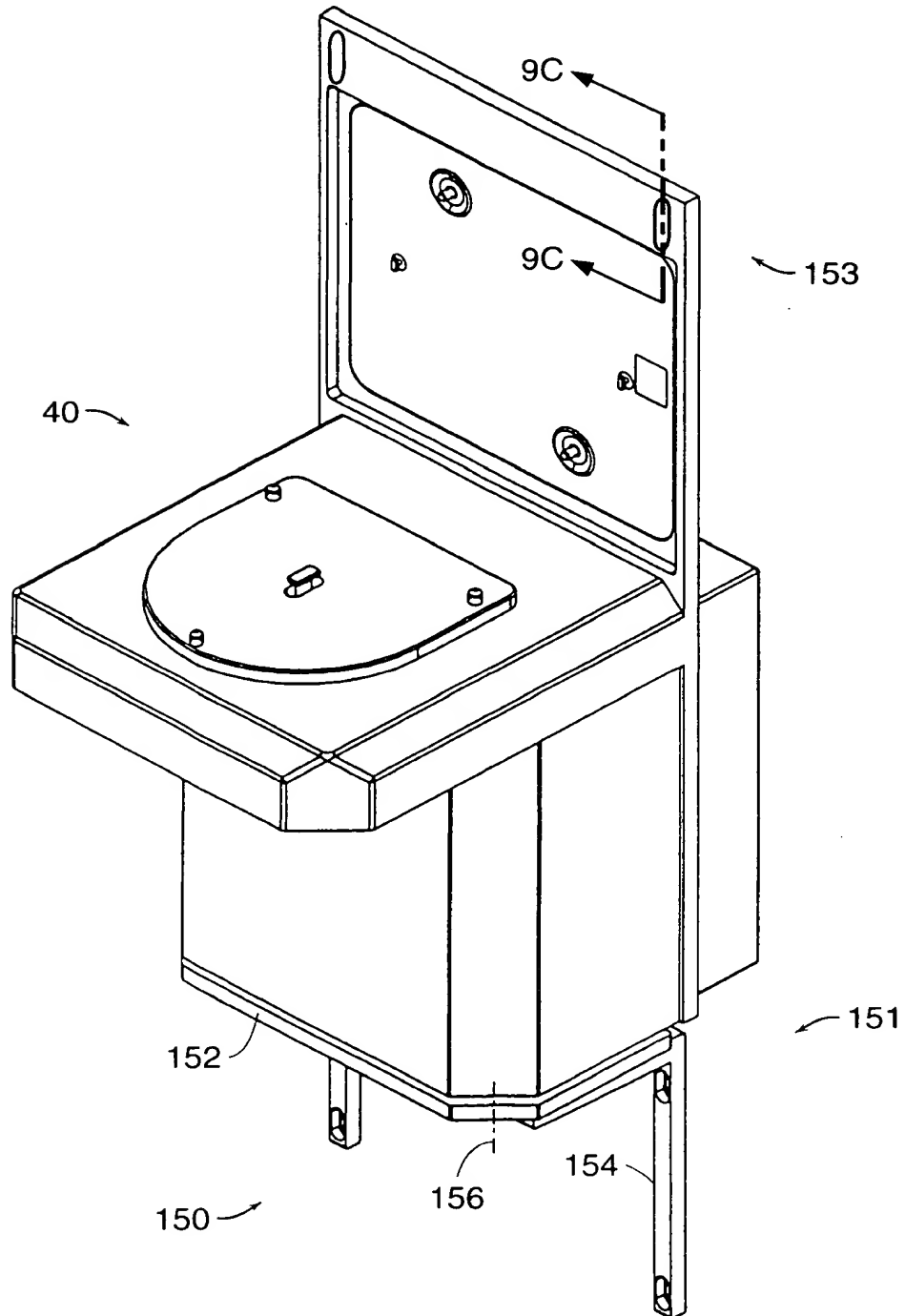


FIG. 9A



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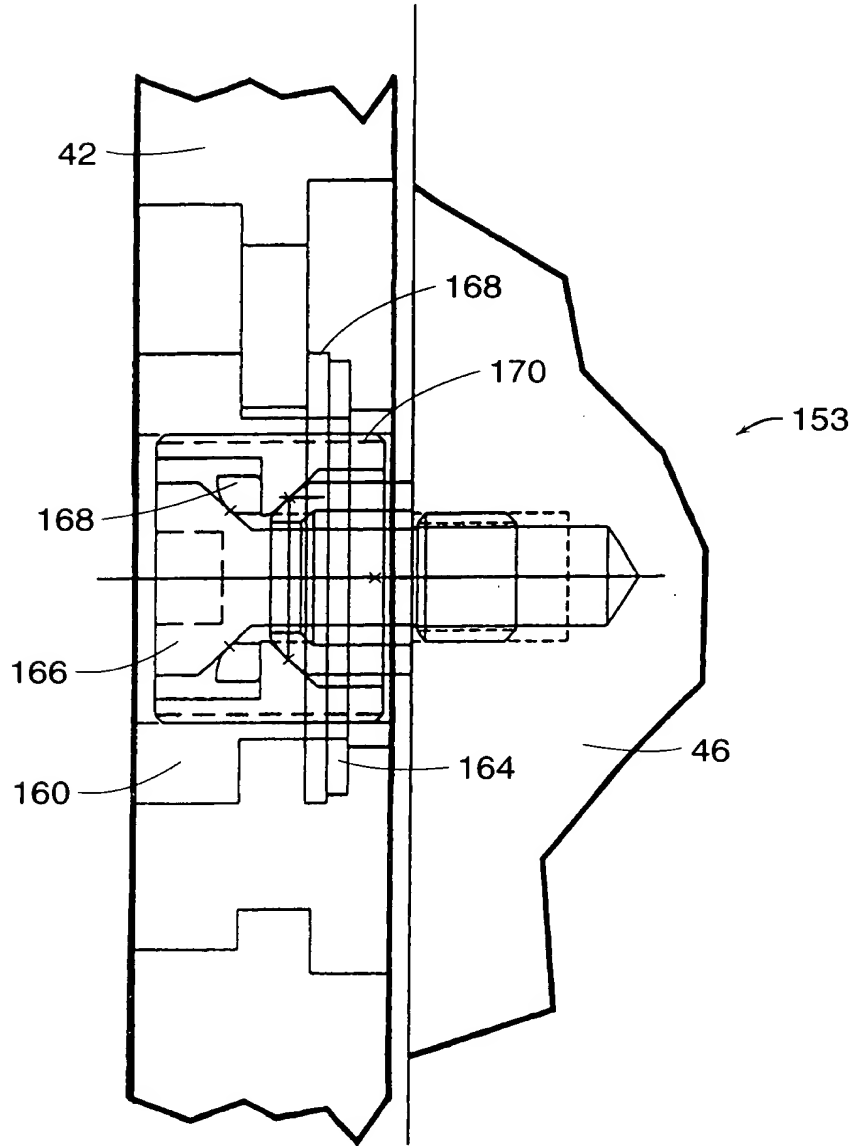


FIG. 9C

APPROVED	O.G. FIG.	
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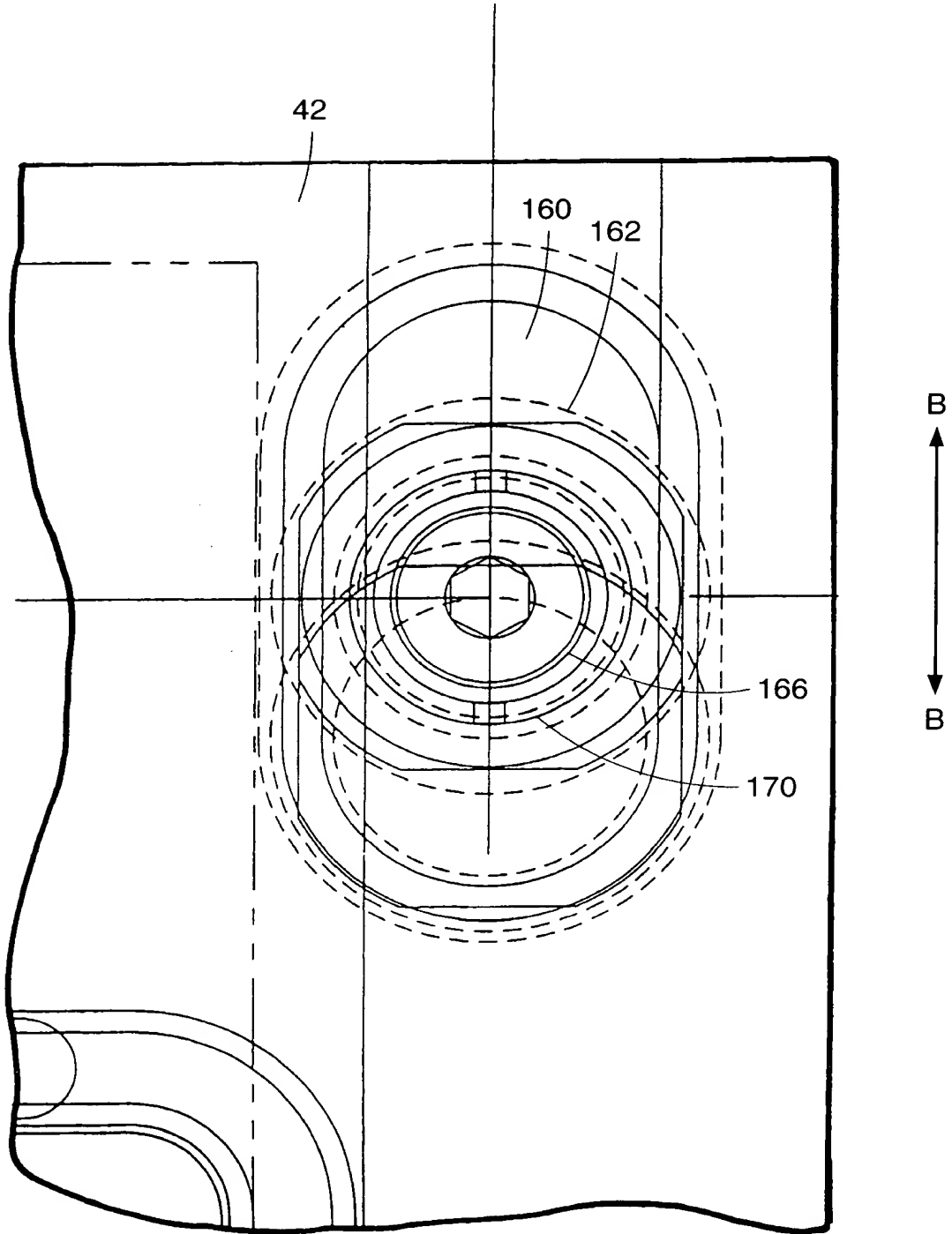


FIG. 9D

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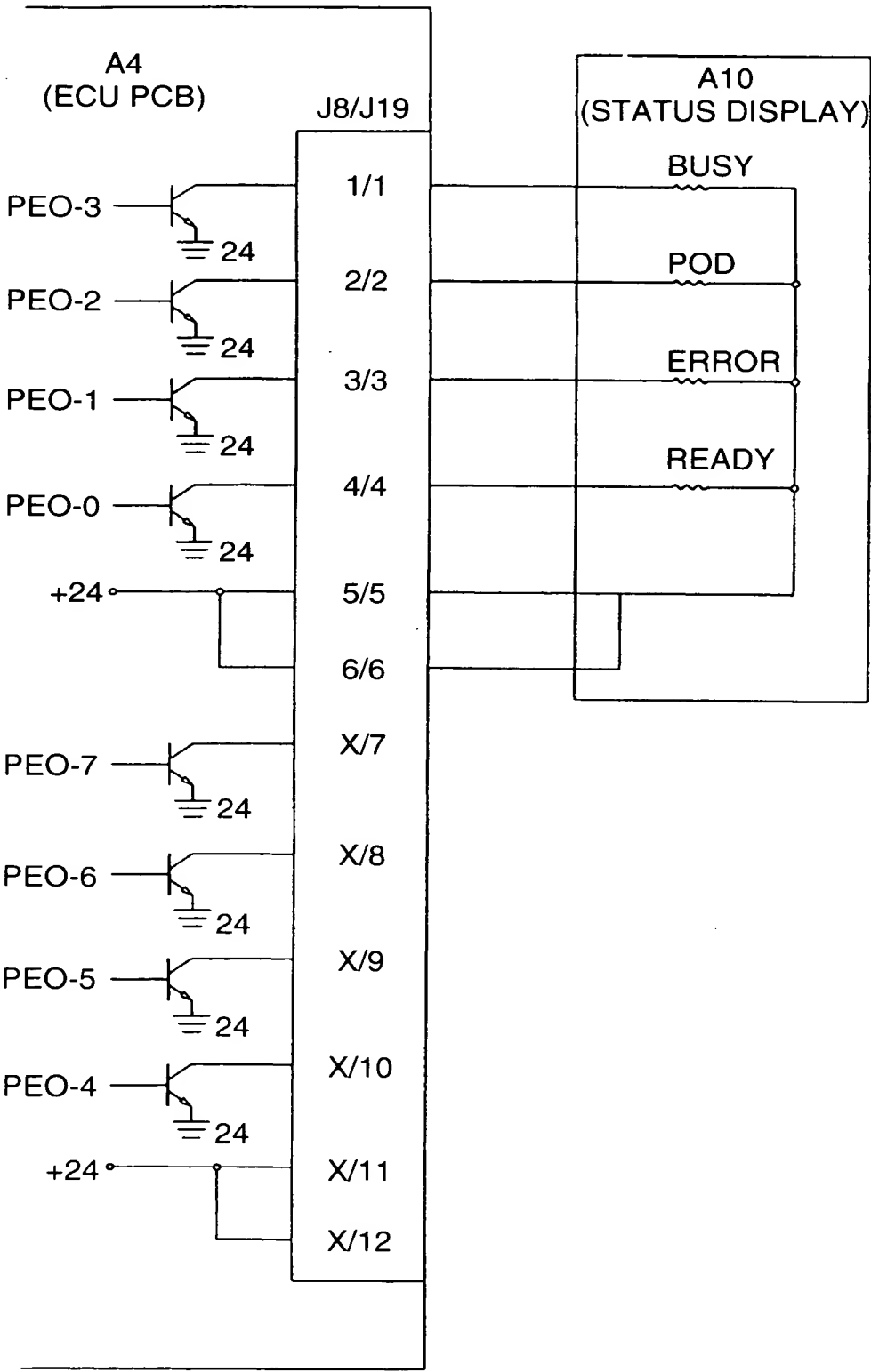


FIG. 10A

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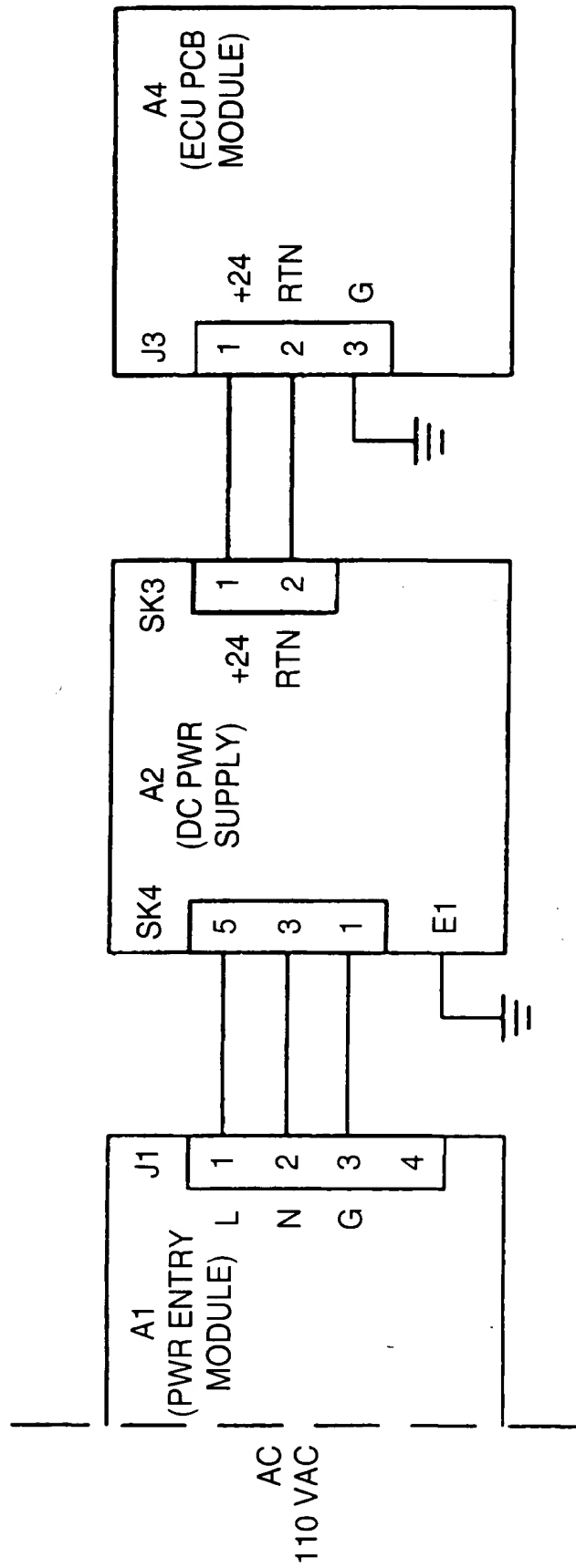


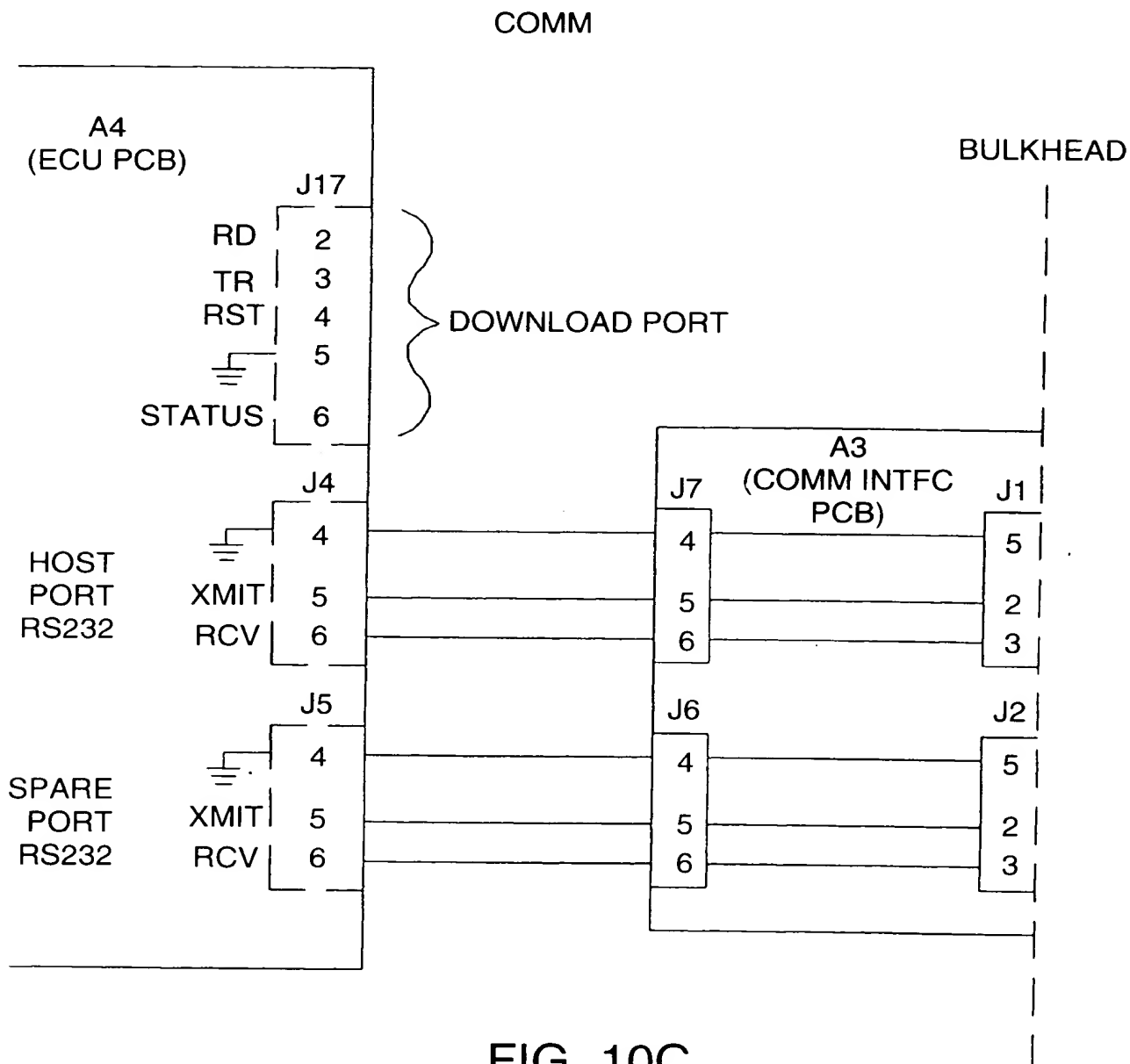
FIG. 10B

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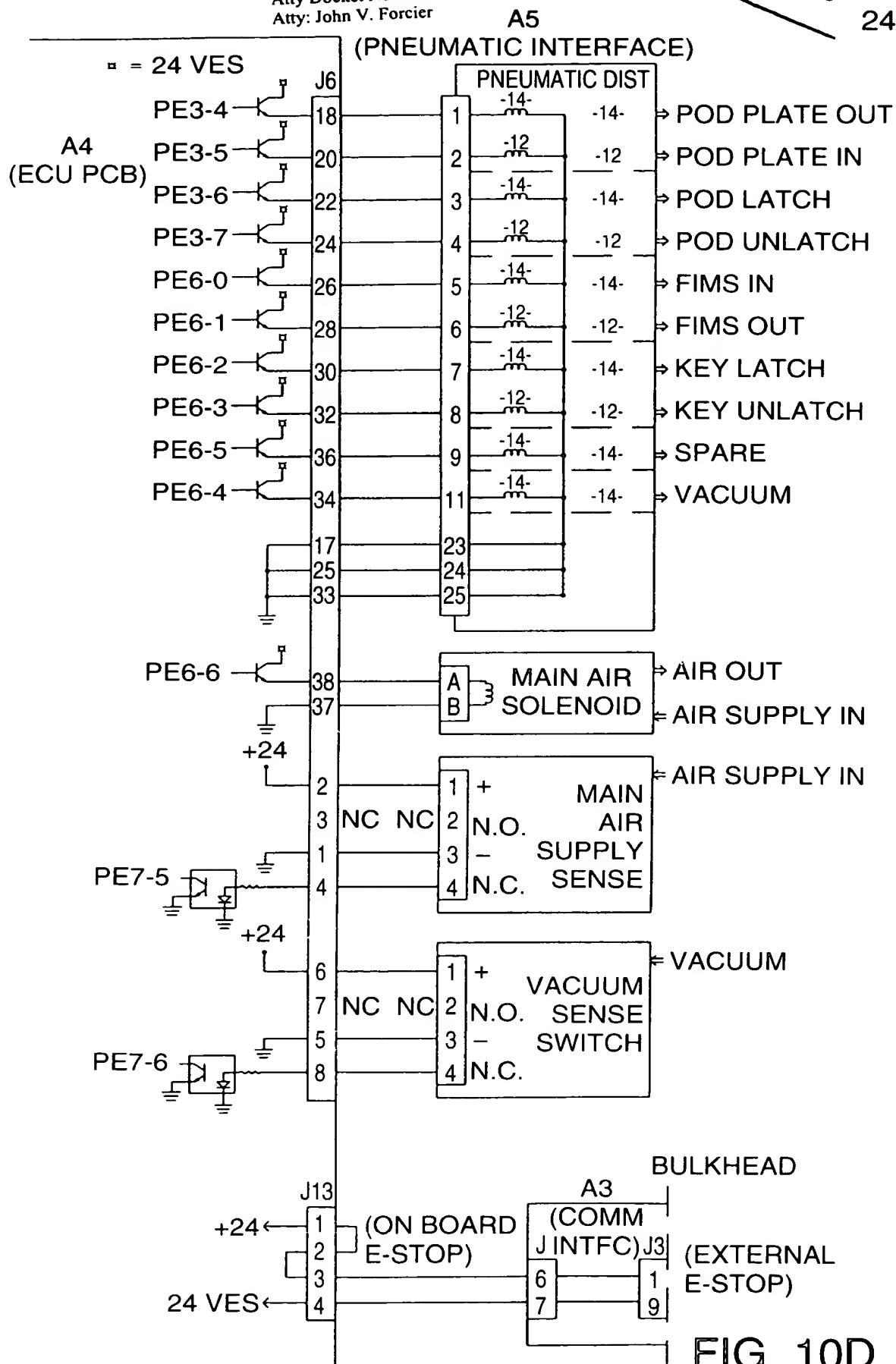
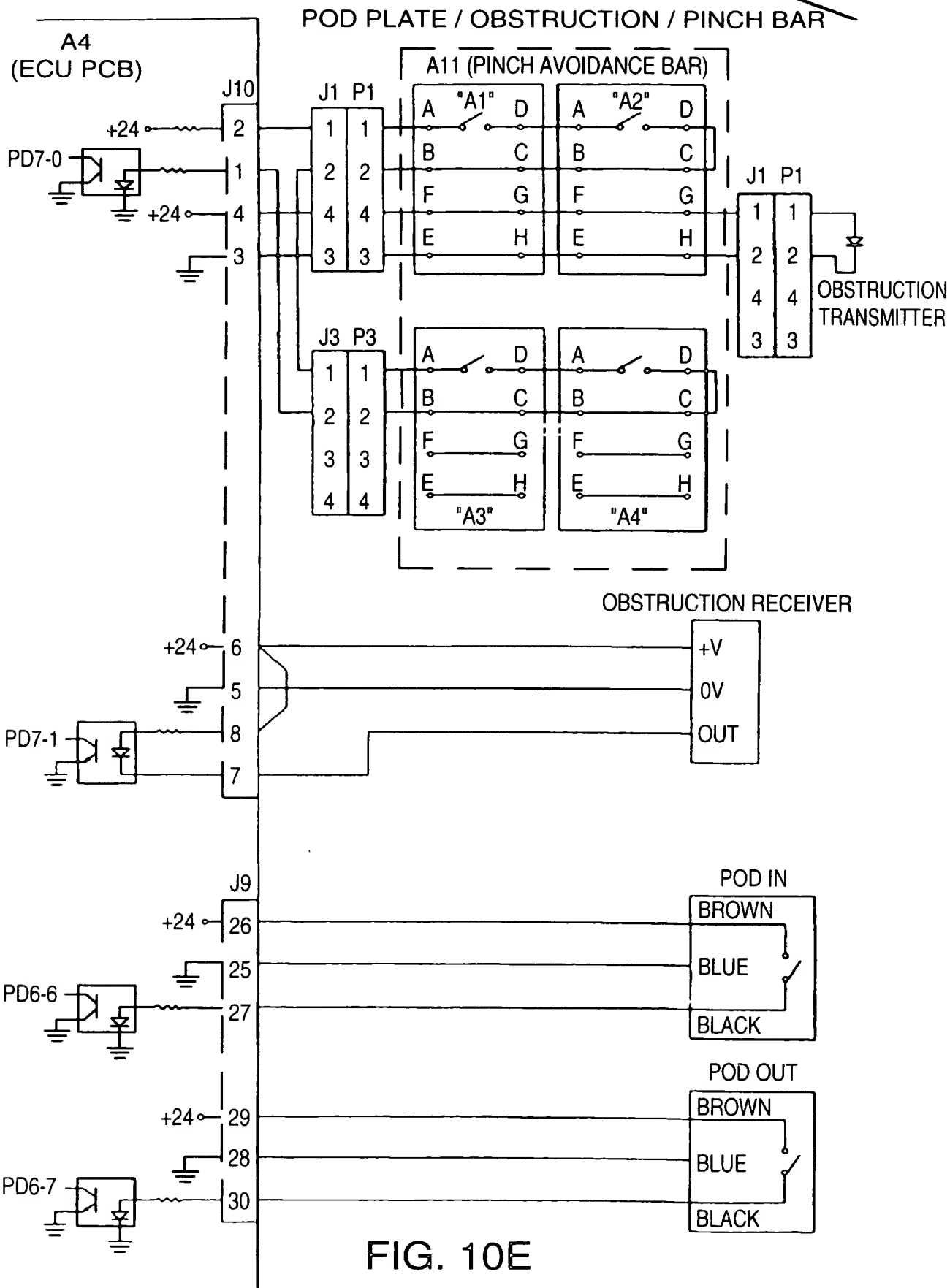
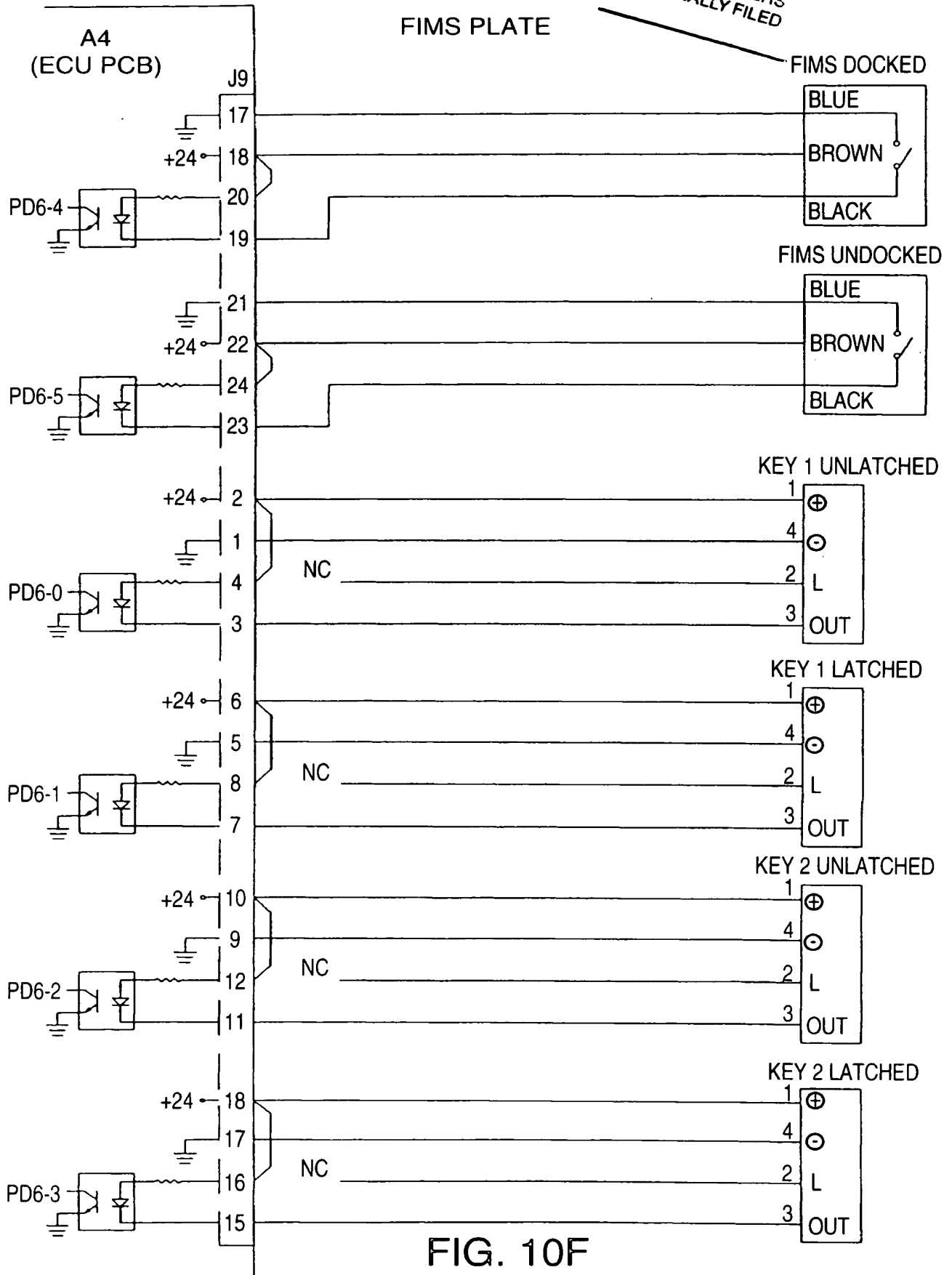


FIG. 10D







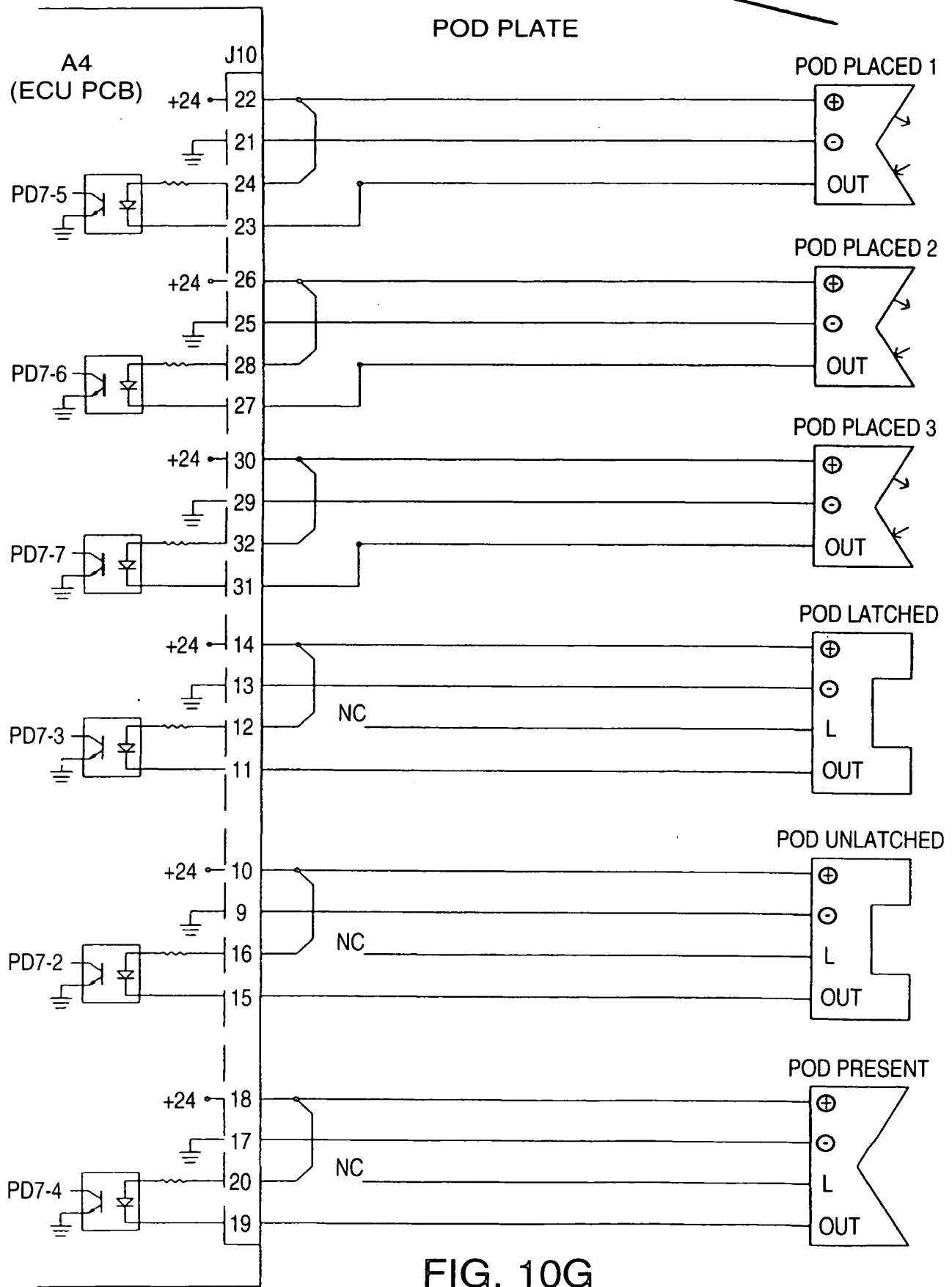


FIG. 10G

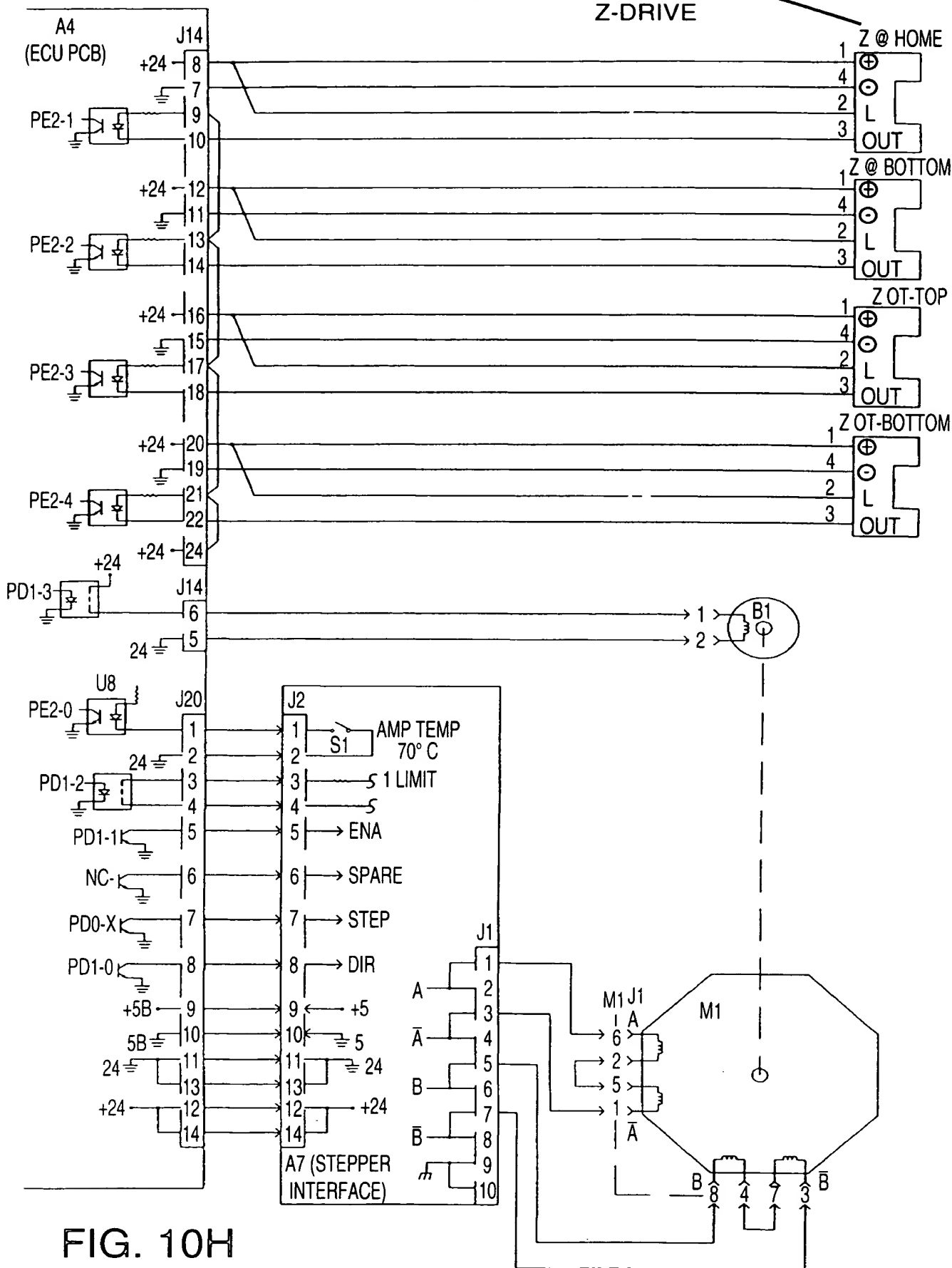


FIG. 10H